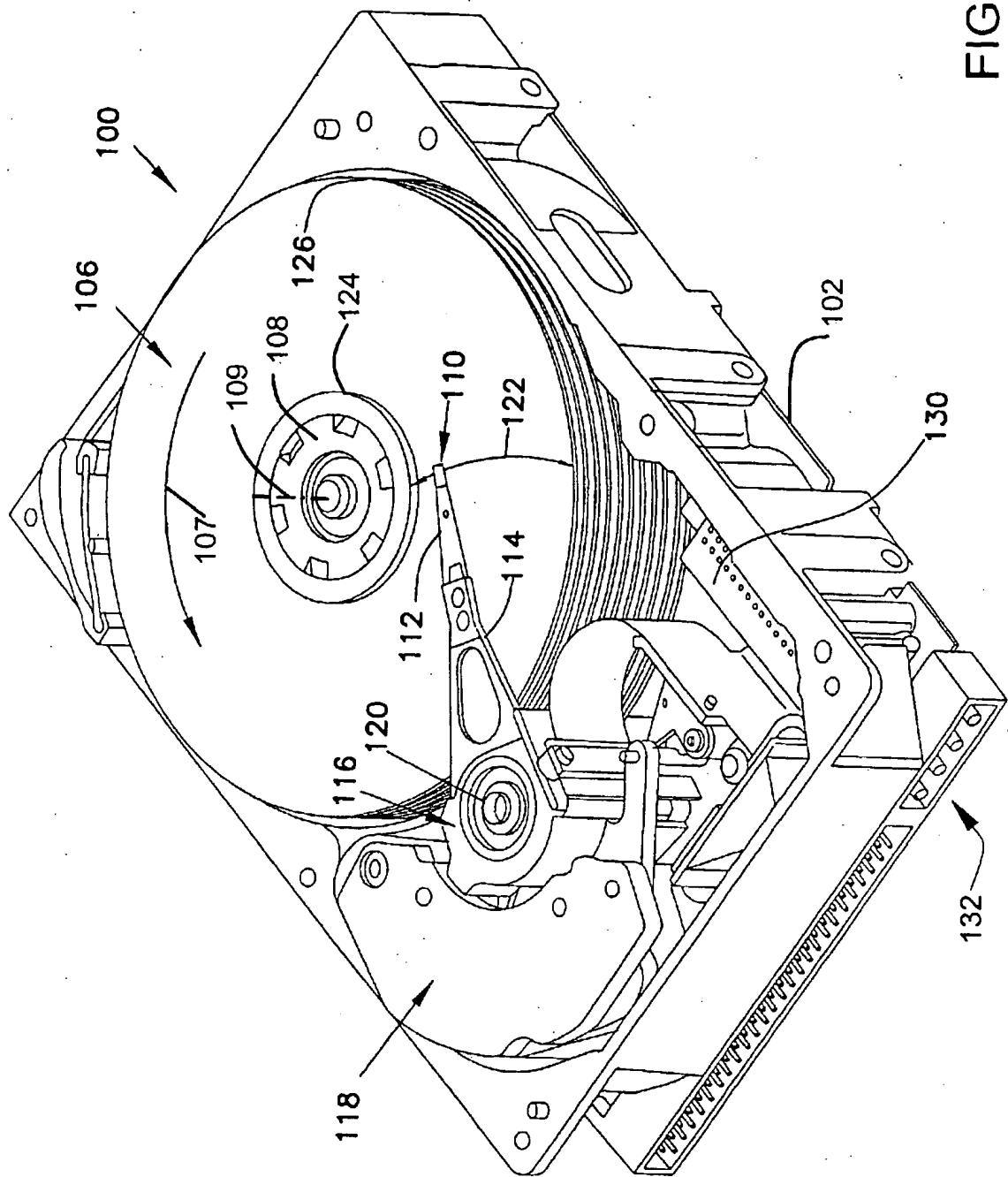


FIG. 1



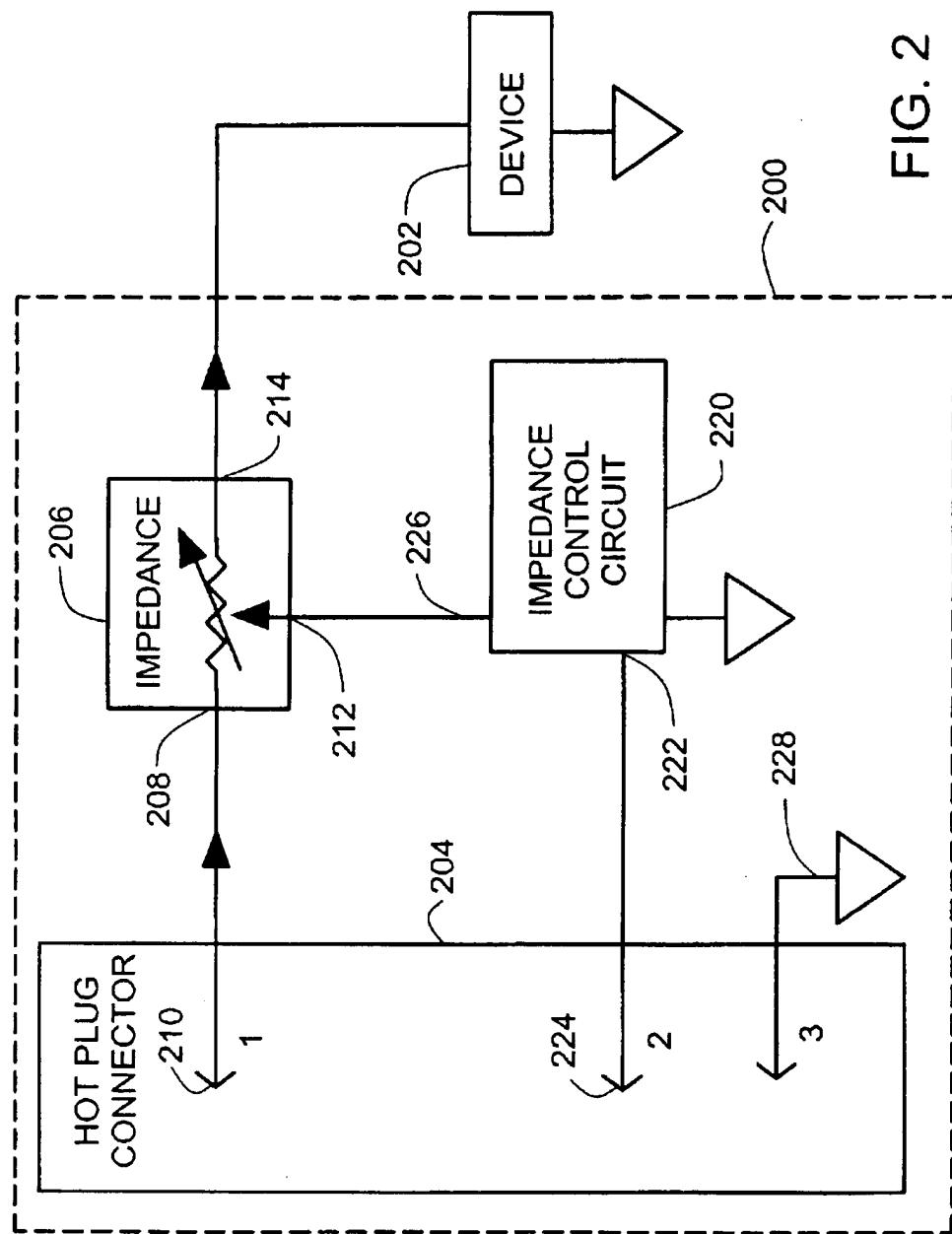
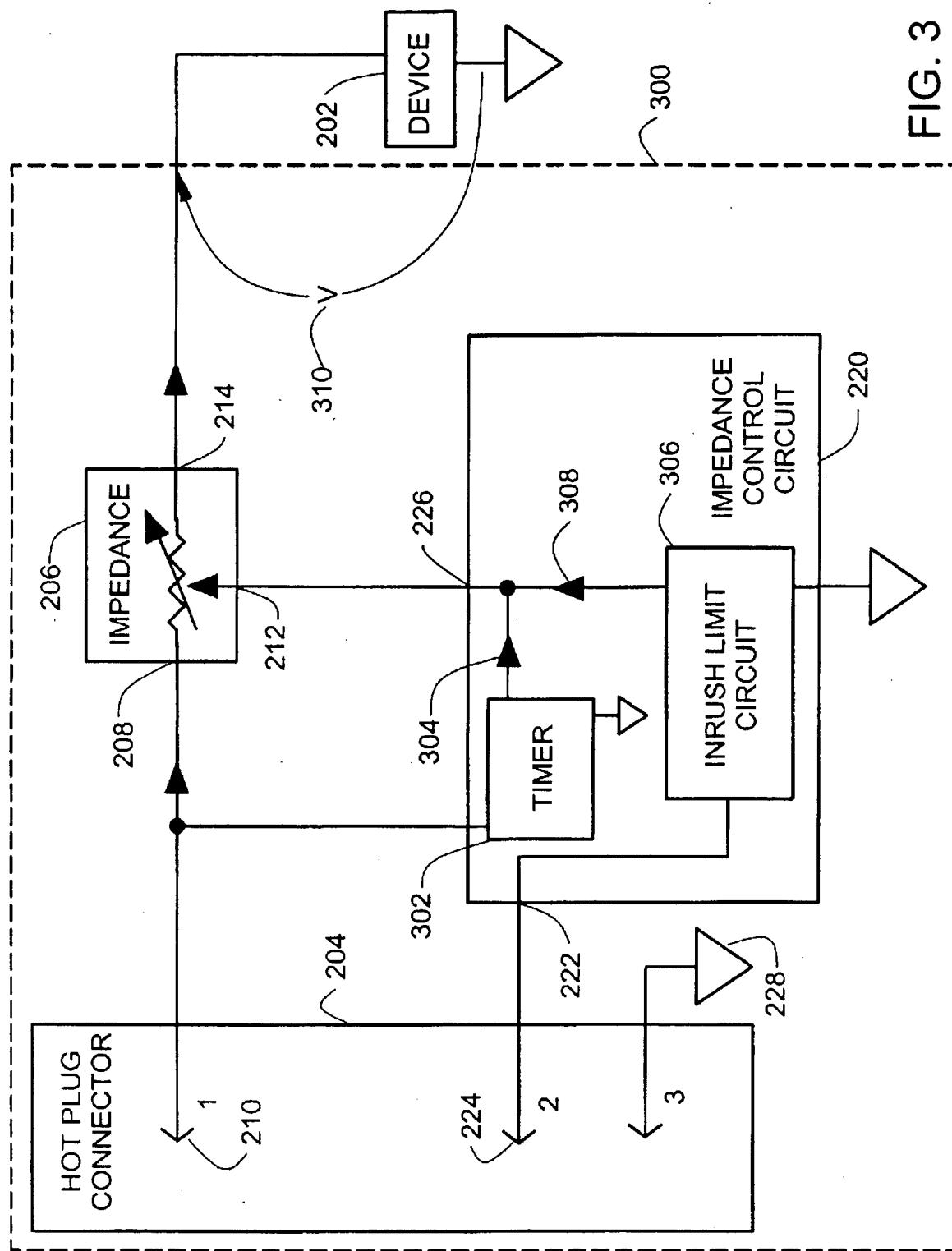


FIG. 2



3
FIG.

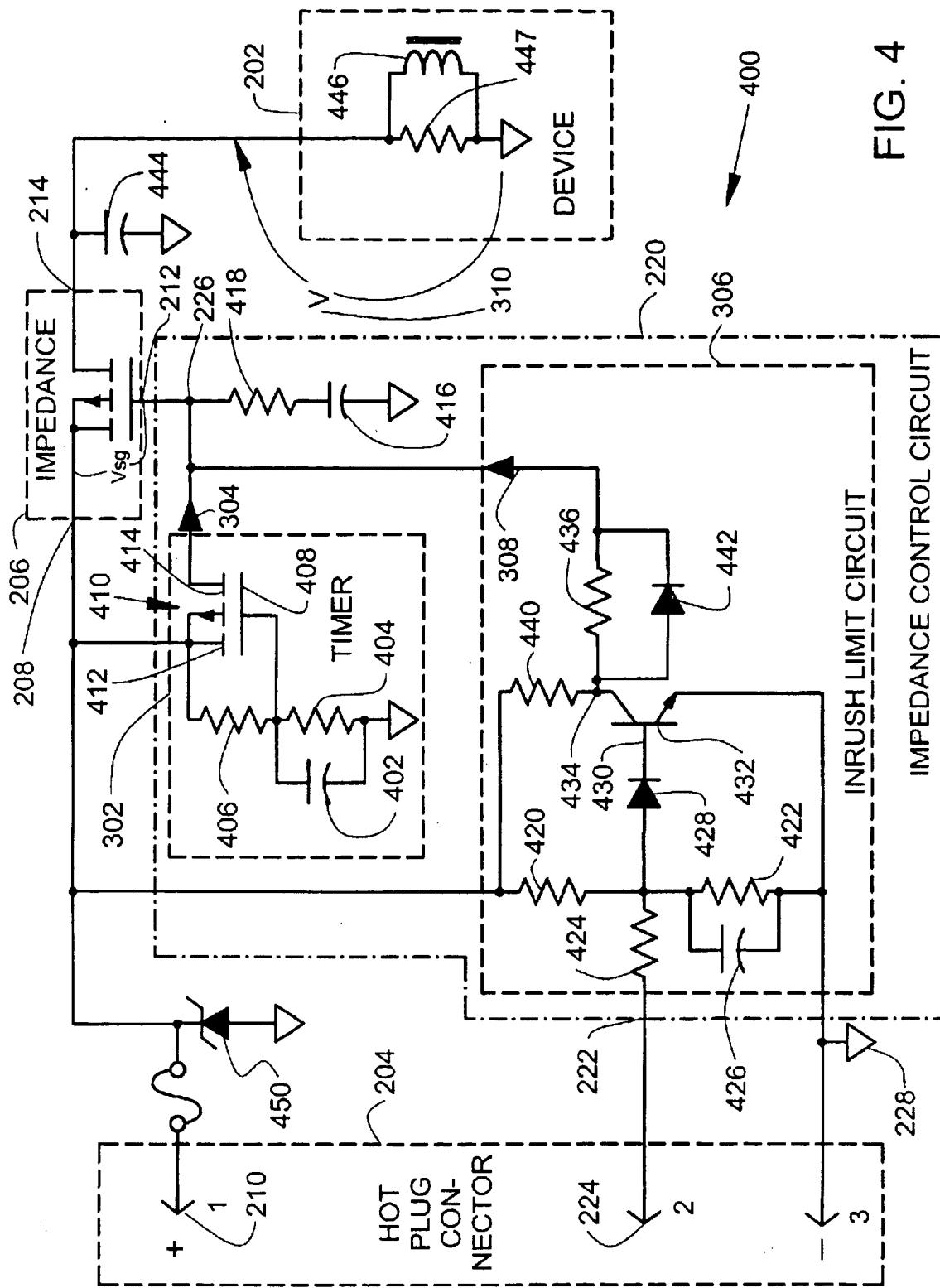


FIG. 4

INRUSH LIMIT CIRCUIT

IMPEDANCE CONTROL CIRCUIT

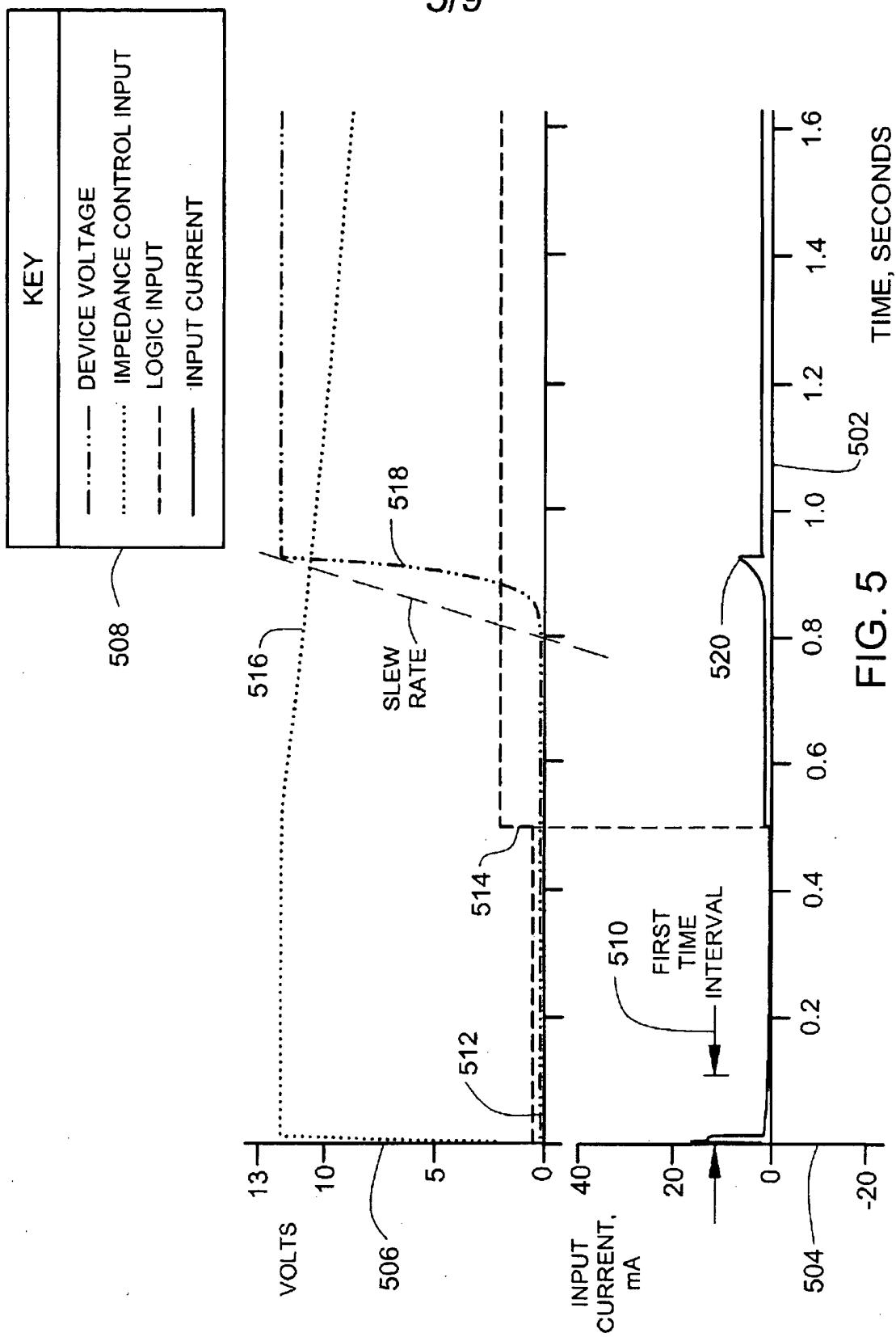


FIG. 5

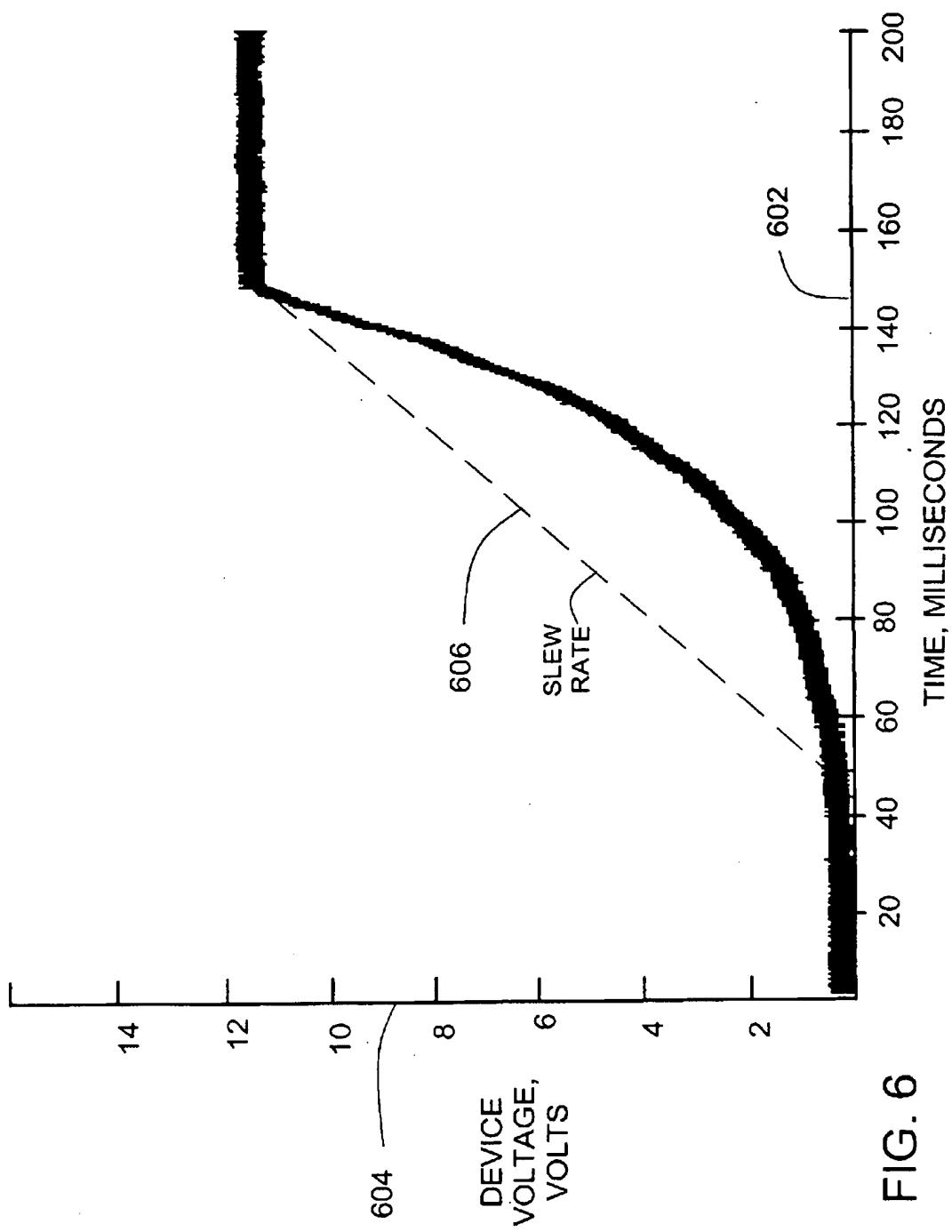


FIG. 6

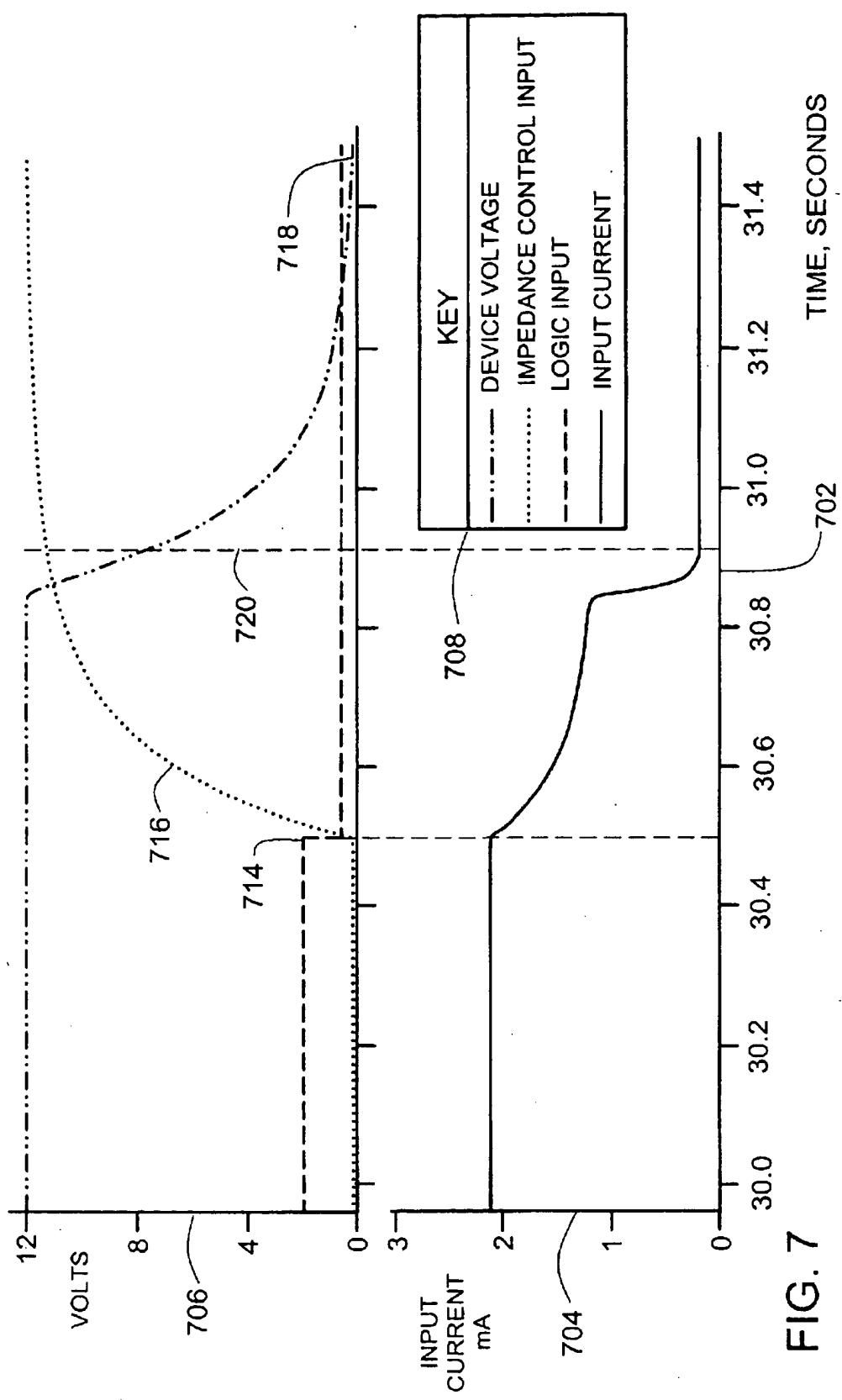


FIG. 7

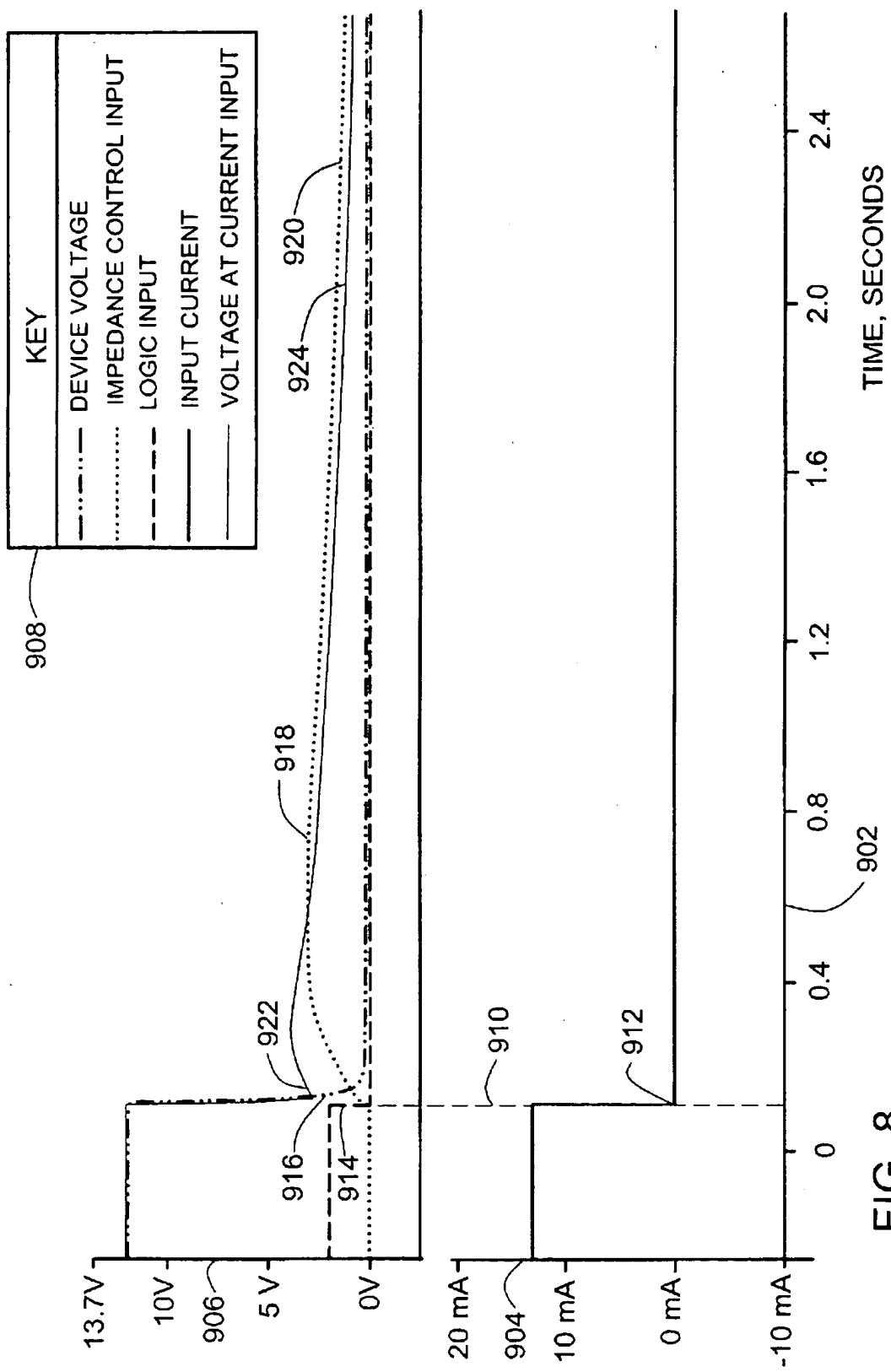


FIG. 8

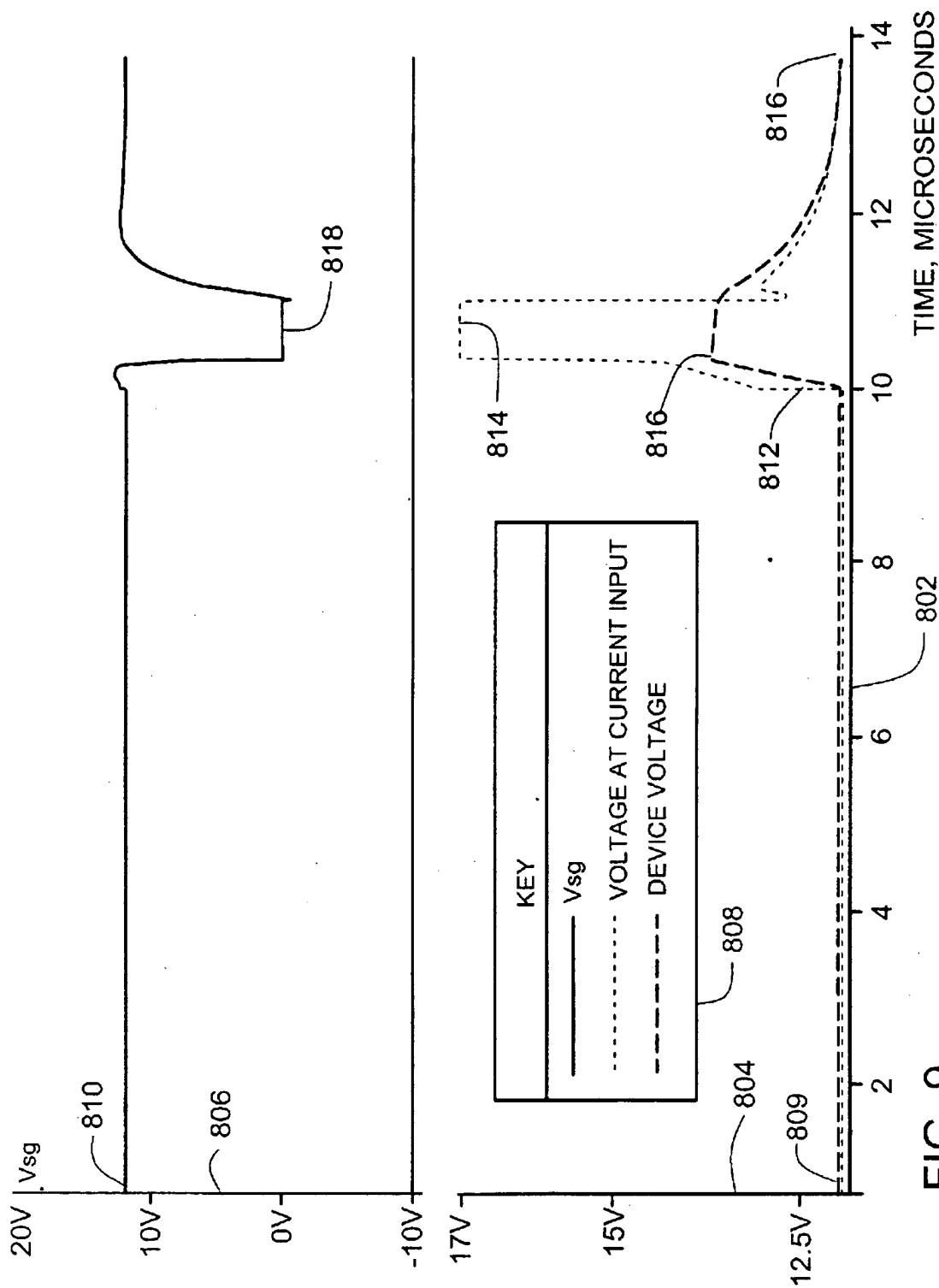


FIG. 9